



Parikh 2

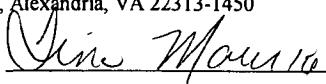


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

5      Applicant(s): Parag Parikh  
Case:            2  
Serial No.:     10/770,046  
Filing Date:   February 2, 2004  
Group:          2817  
10     Examiner: Arnold M. Kinkead

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. 1450, Alexandria, VA 22313-1450

Signature:  Date: October 17, 2005

Title:        Clock Generation Circuits Providing Slew of Clock Frequency

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AMENDMENT AND RESPONSE TO OFFICE ACTION

Mail Stop Amendment  
Commissioner for Patents  
20     P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

25        In response to the outstanding Office Action, dated July 14, 2005, please amend  
the above-identified patent application as follows: